



FAST-X FRAME GRABBER

The Fast-X, is a two thirds raw-length PCI board with four GigE ports and/ or three to six 85 MHz Basic Camera Link Channels which provide input for high speed cameras. The front end data is formatted and preprocessed by a FPGA before being sent to the memory subsection, processor, GigE interfaces or other outputs. Finally, the Fast-X interfaces to the host computer through a PCI-X interface for state-of-the-art data acquisition. I/O options include one to four UXGA input channels each with up to 205 MHz data acquisition rates.



The Future of Image Aquisition and Processing

Fast-X PCI-X Key Features:

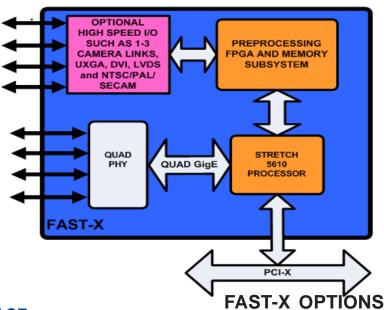
- Raw PCI form factor board with 1-2GB dedicated high speed DDRAM for sustained real-time on-board storage
- STRETCH 5610 processor standard with 4 GigE interfaces for 100MBytes/sec per channel actual transfer I/O
- Collects data from up to six asynchronous Basic Camera (channel) Link channels, four UXGA 205 MHz channels, or up to 52 GP inputs, while providing 12 GP outputs, at up to 166 MHz
- Programmable FPGA for I/O interface configuration and processing
- PCI-X bus interface
- Supported by standard firmware development tools, including fully optimized basic data manipulation, data formatting and image processing routines





FAST-X BOARDS





PCI-X INTERFACE

- Data width 32/64 bits
- Peak tranfer rate of 133 MHz

PROCESSOR OPTIONS

■ One STRETCH processor with up to 2 GB of PC3200 DDRAM memory

CAMERA CONTROL

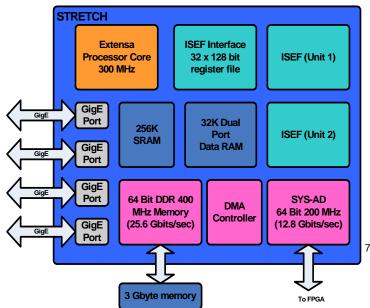
- Serial port- Asynch., RS-232 600-19,200 Baud
- Camera Link controls (optional)

MEMORY OPTION

One to two gigabytes of DDRAM memory directly connected to the input fpga for high-speed input

I/O OPTIONS

- Three to six 85 MHz 28- bit Camera (channel) Link I/O channels
- output options. Current option are a six 85 MHz bit Basic Camera (channel) Link I/O, a two to four UXGA input up to 205 MHz per channel, or collect data from two connectors of up to 52 GP inputs, while providing 12 GP outputs, at up to 166 MHz.





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